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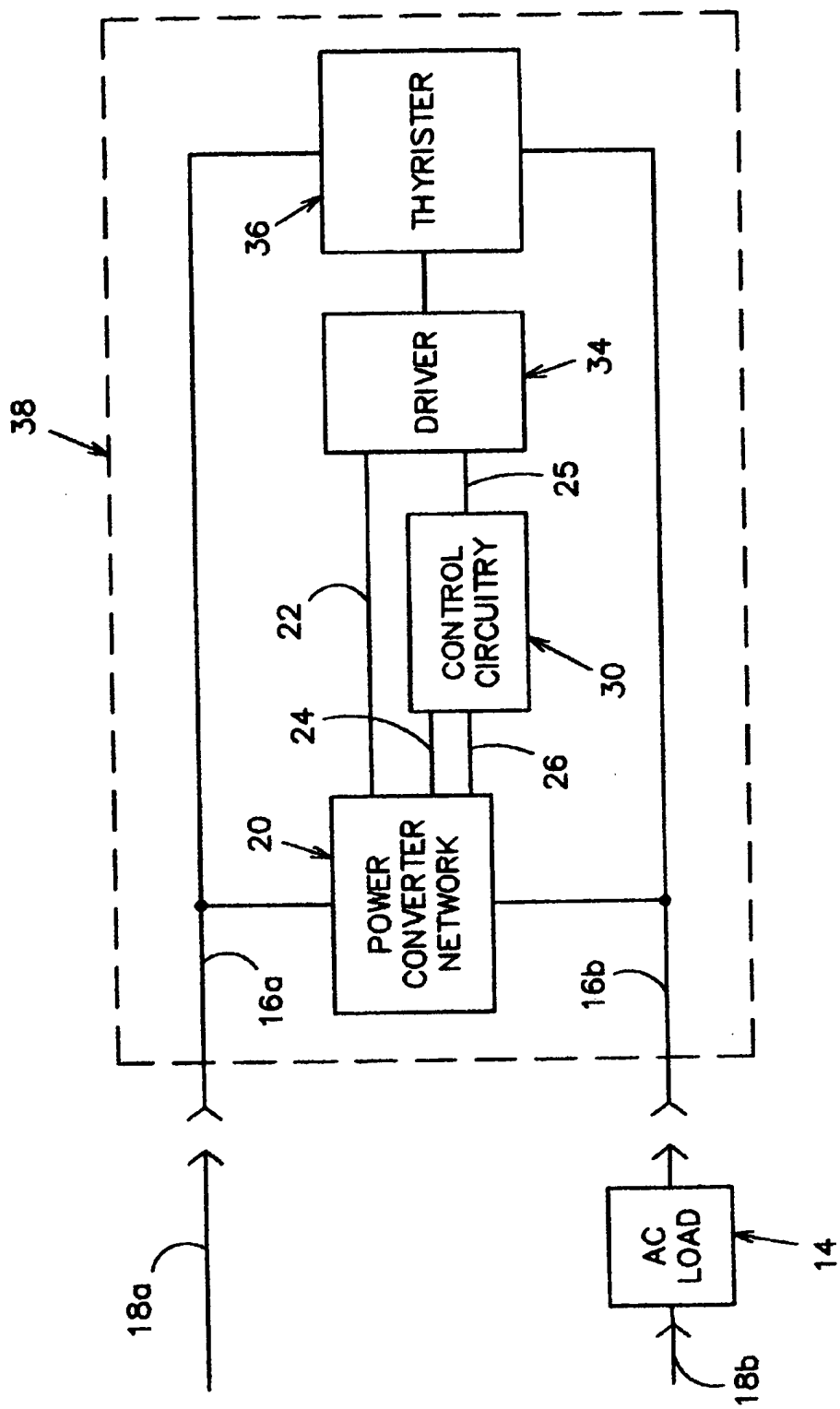


FIG. 1

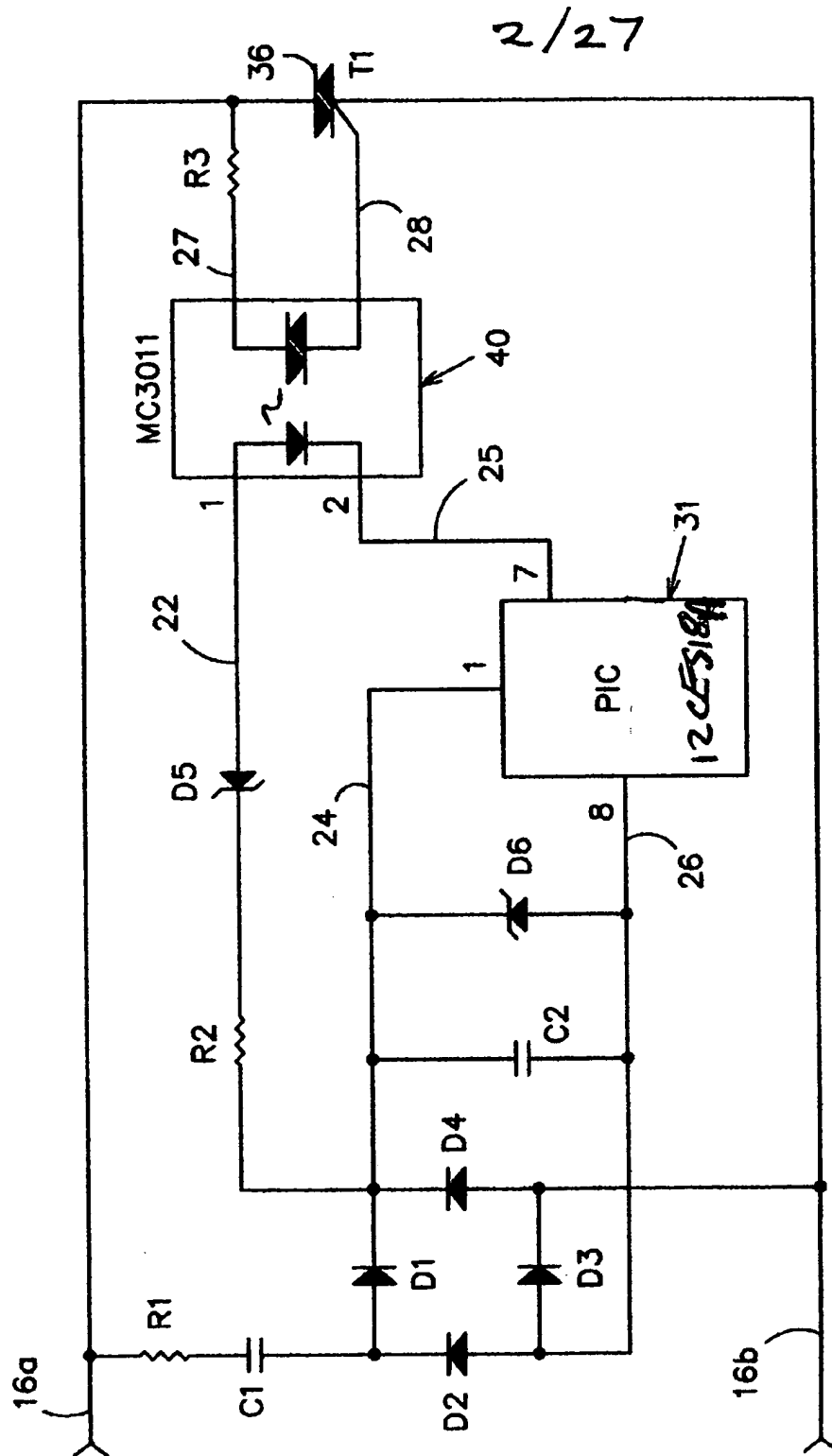


FIG. 2

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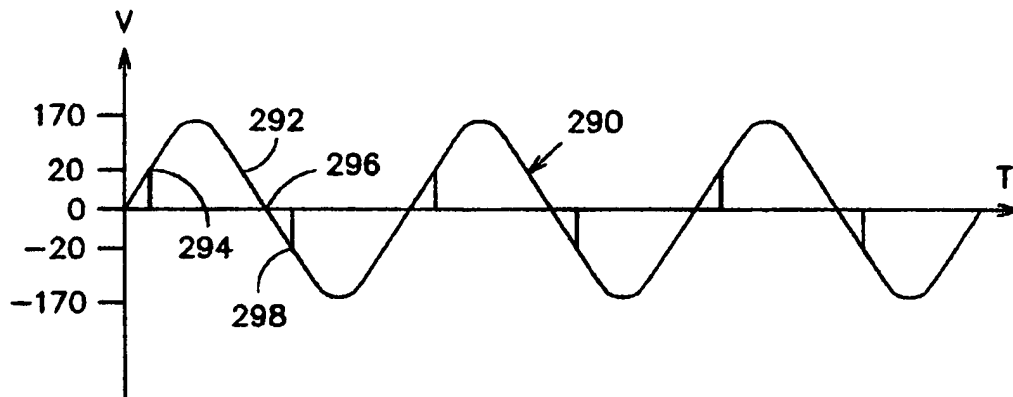


FIG. 3A

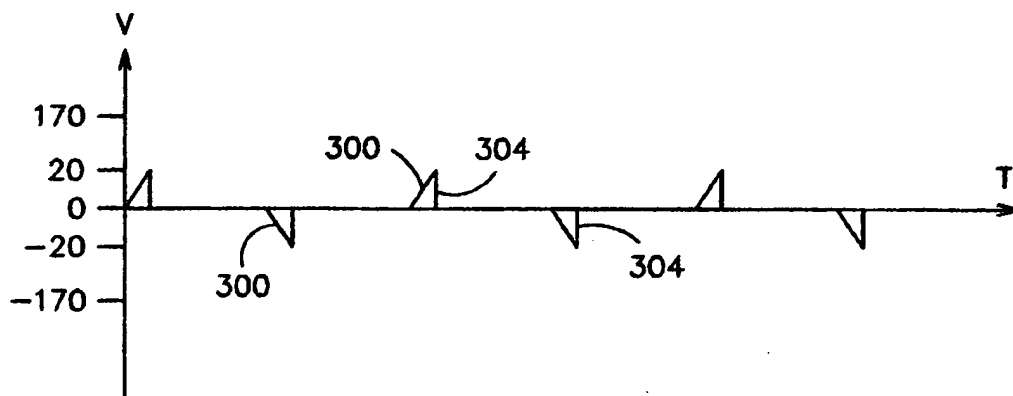


FIG. 3B

FIG. 4

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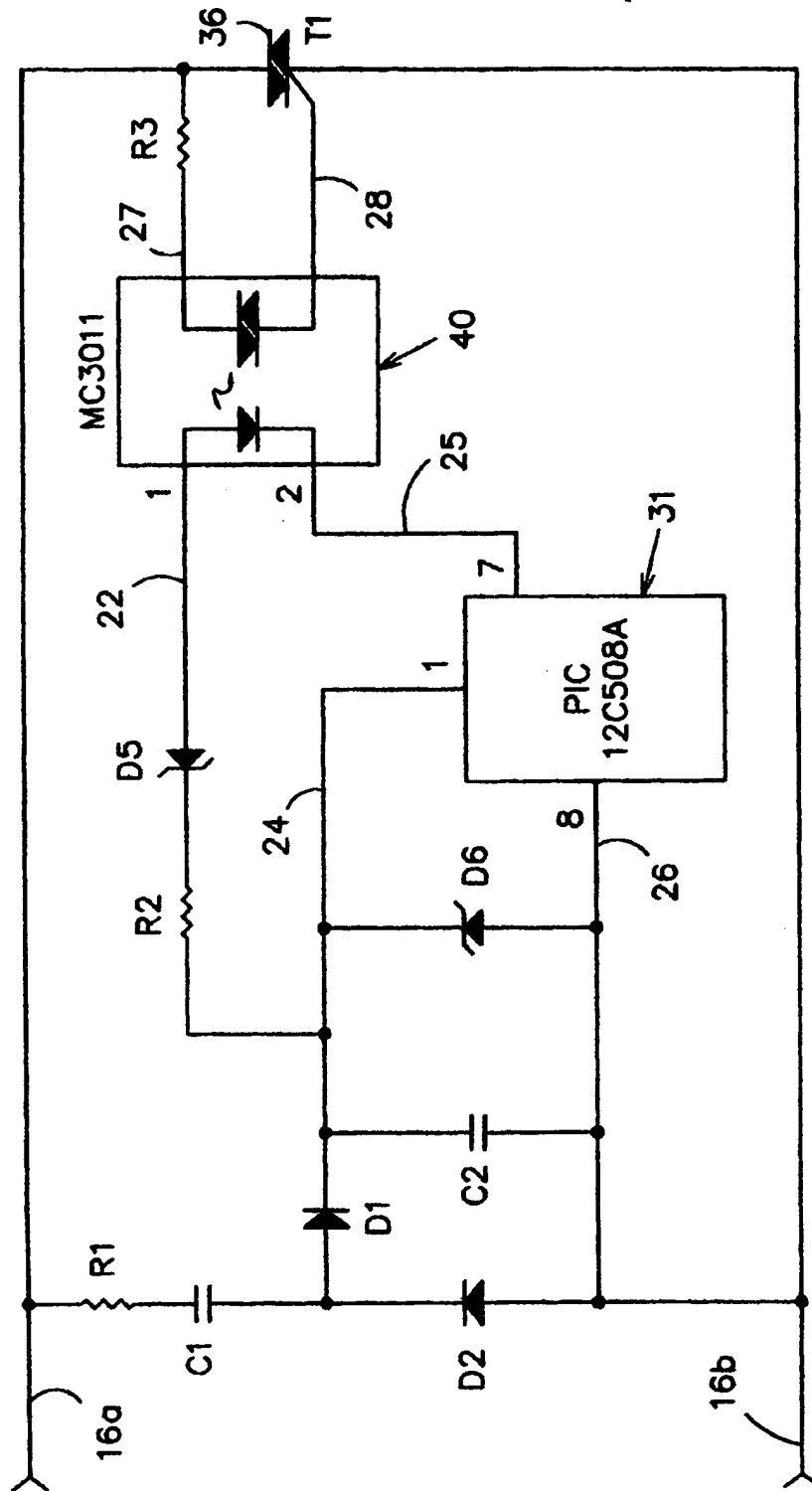


FIG. 5

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FIG. 6

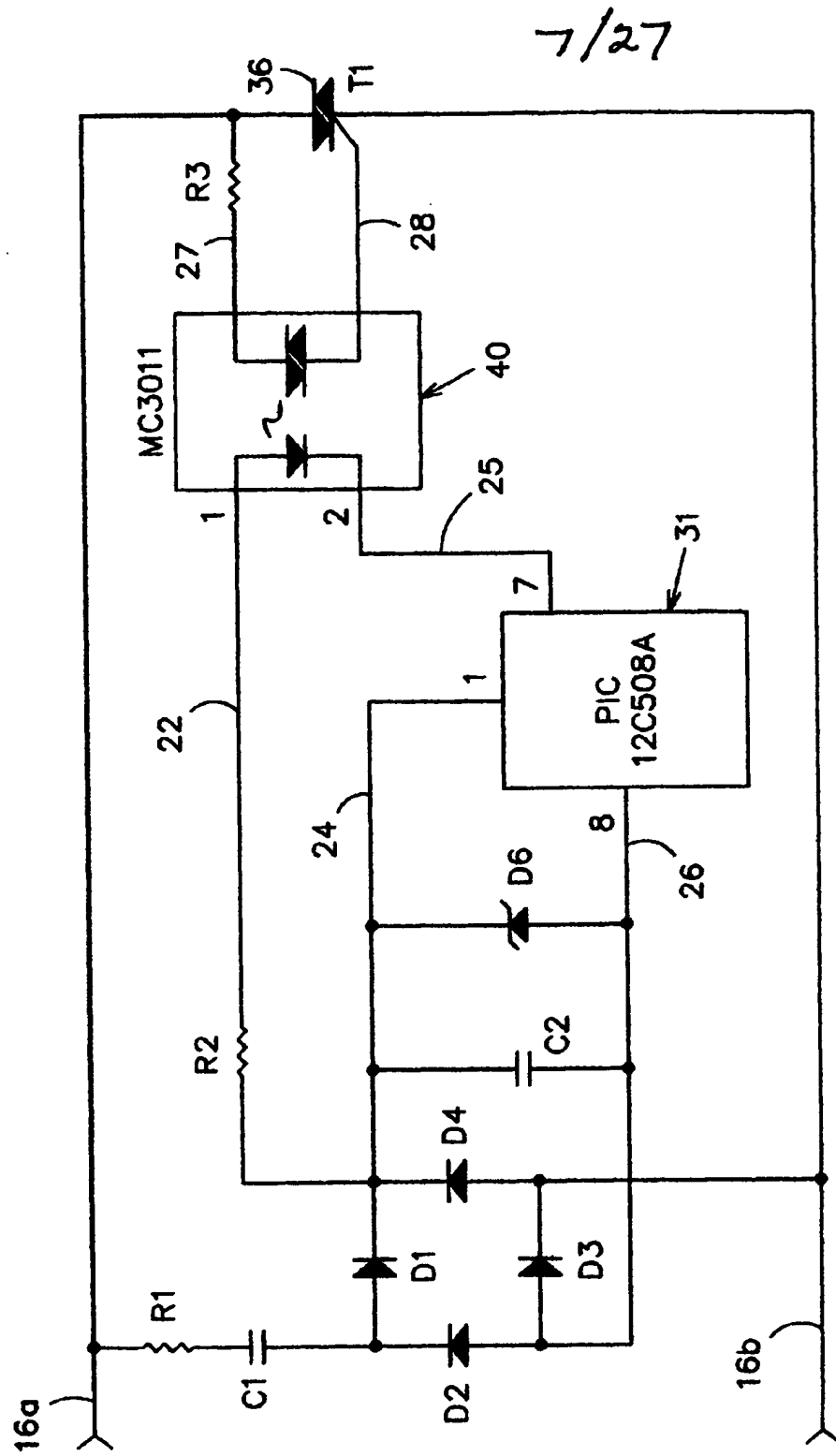


FIG. 7

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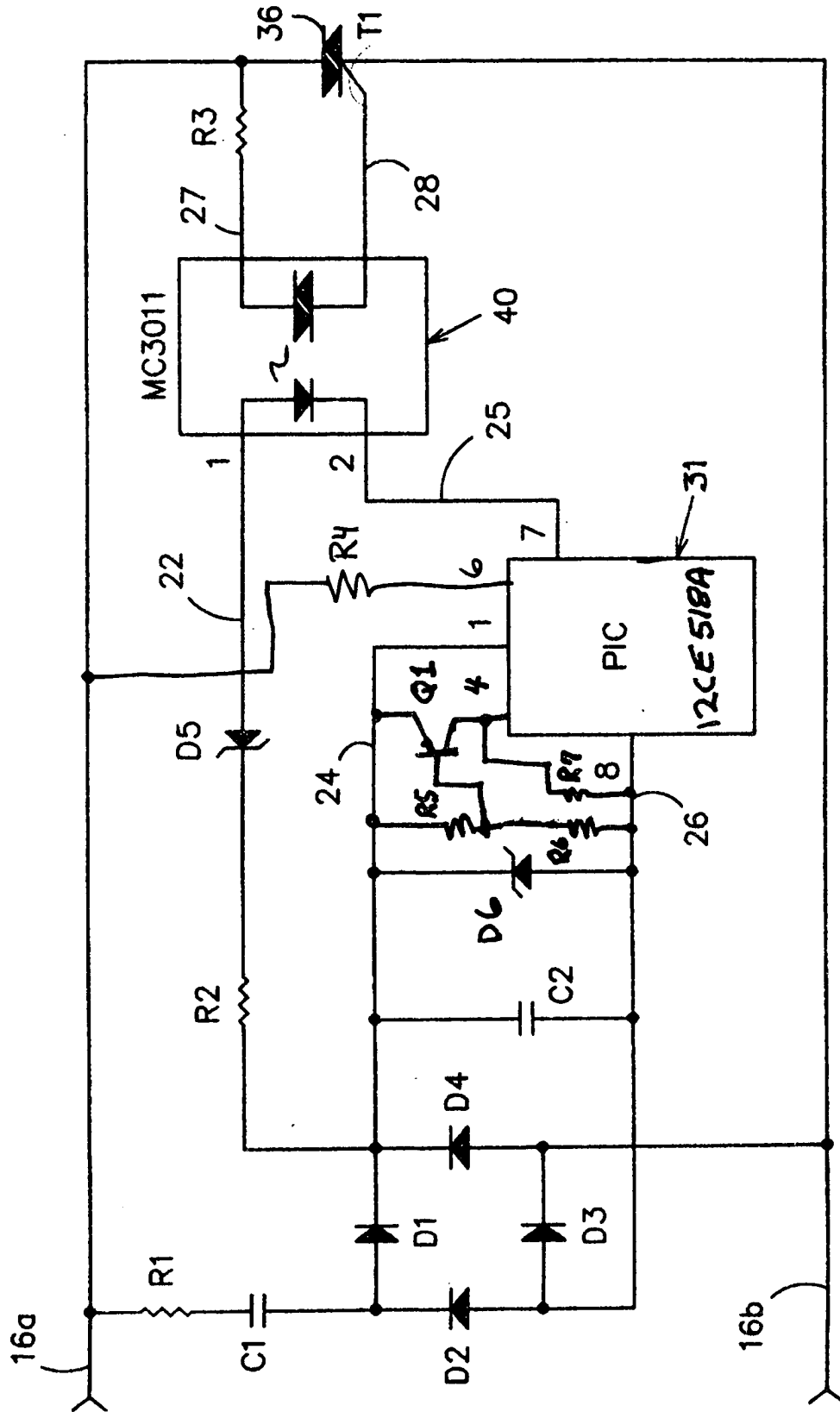


FIG. 2



The diagram illustrates a control system architecture. A dashed box labeled 90 encloses the core control components: a MICRO (microprocessor) block, a DRIVER block, and a TRIAC block. The MICRO block is connected to a DRIVER block, which in turn controls the TRIAC block. A ZERO block is also connected to the MICRO block. A POWER NETWORK block is connected to the MICRO block. The MICRO block is also connected to a series of five output blocks labeled 70, 76, 72, 74, and 78. The TRIAC block is connected to a motor symbol labeled 60. A speaker symbol labeled 80 is connected to the DRIVER block. A ground symbol labeled 82 is connected to the MICRO block. The entire system is connected to a power source labeled 18a and a ground symbol labeled 18b. The output blocks 70, 76, 72, 74, and 78 are connected to a common ground line labeled 18b. The motor symbol 60 is connected to a power source labeled 18a and a ground symbol labeled 18b. The speaker symbol 80 is connected to a power source labeled 18a and a ground symbol labeled 18b. The ground symbol 18b is connected to the ground symbol 82.

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FIG. 11A

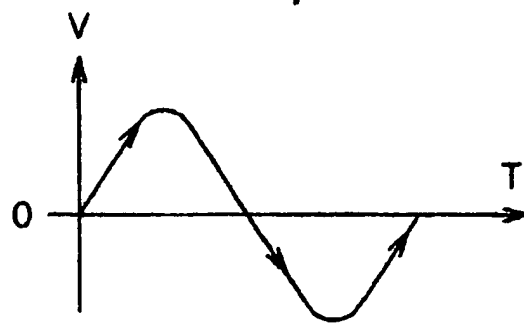


FIG. 11B

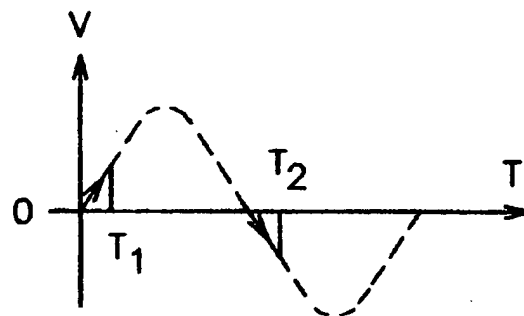


FIG. 11C

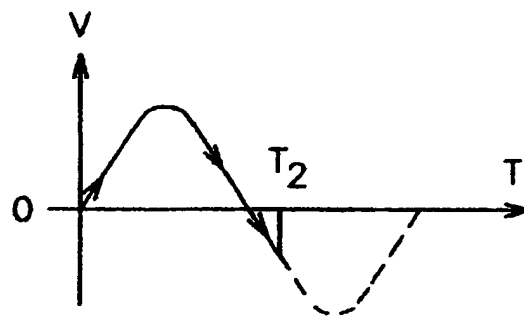
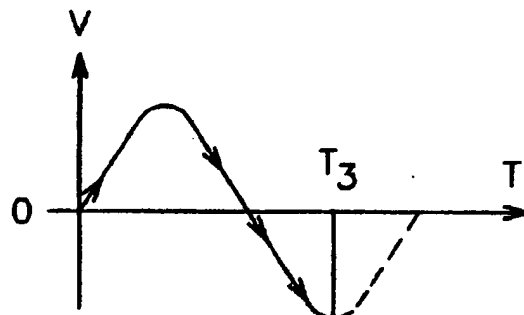


FIG. 11D



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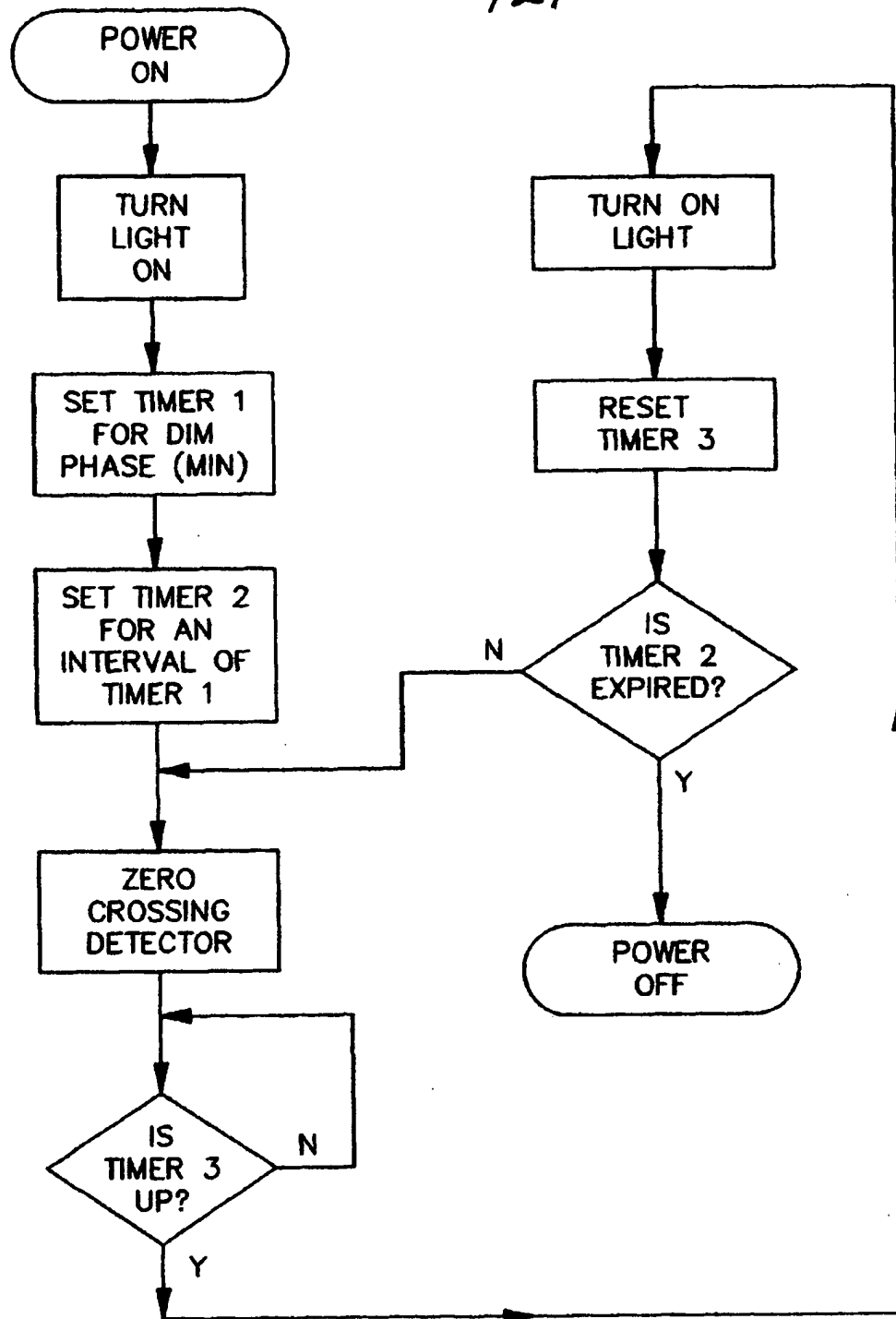


FIG. 12

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FIG. 13

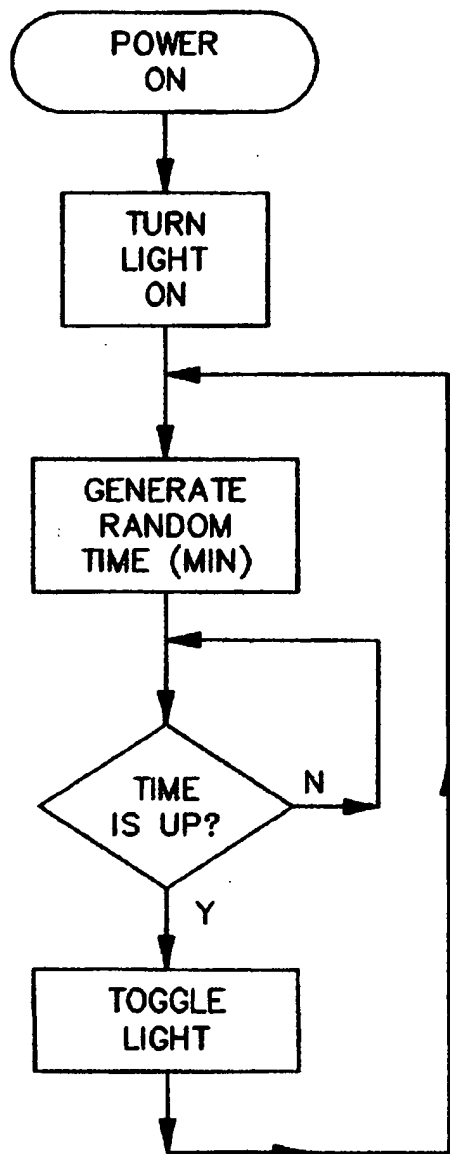
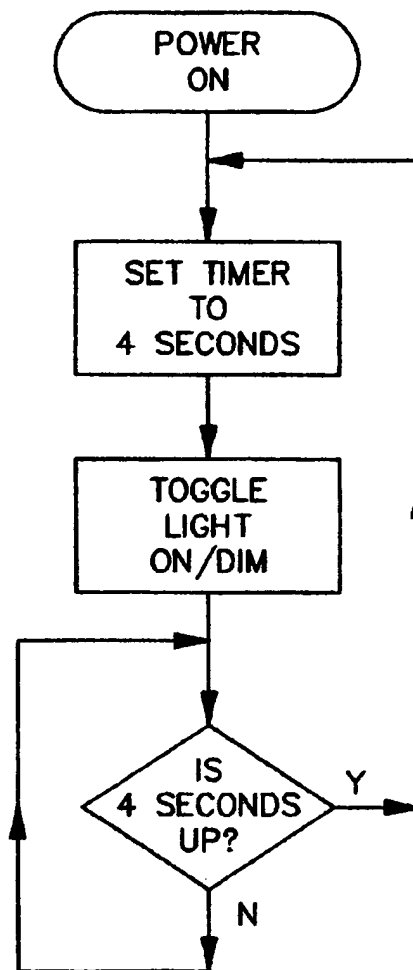


FIG. 14



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FIG. 15

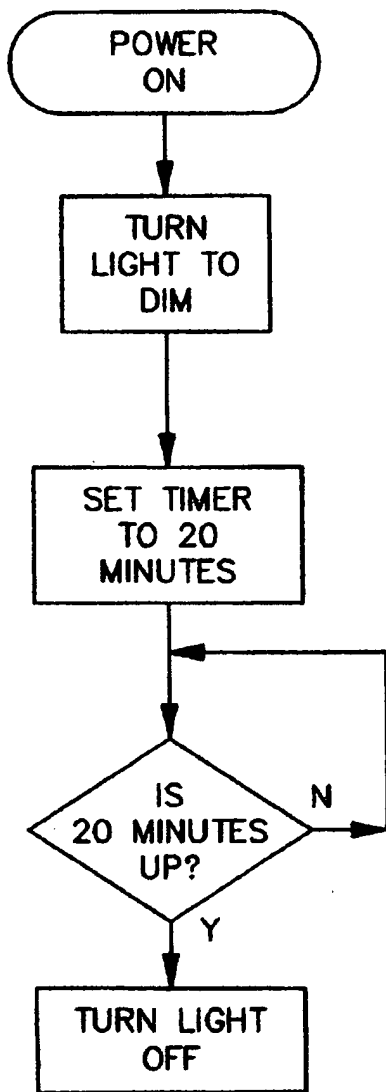
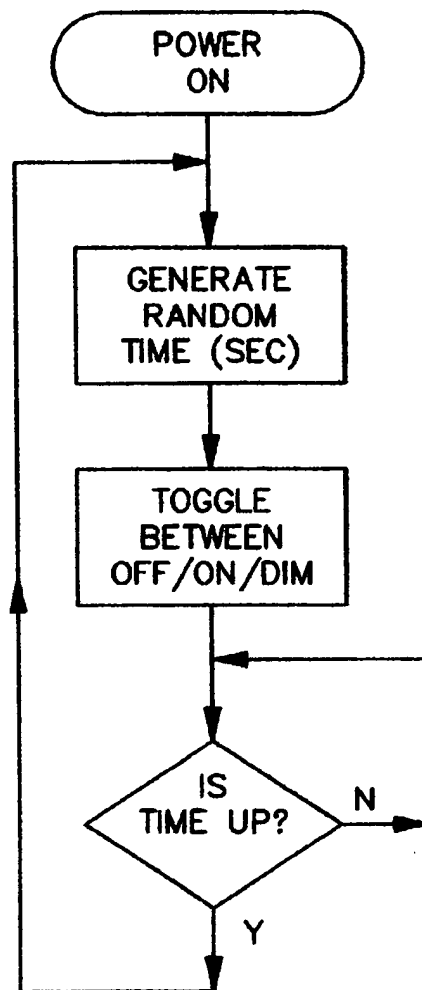


FIG. 16



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FIG. 17

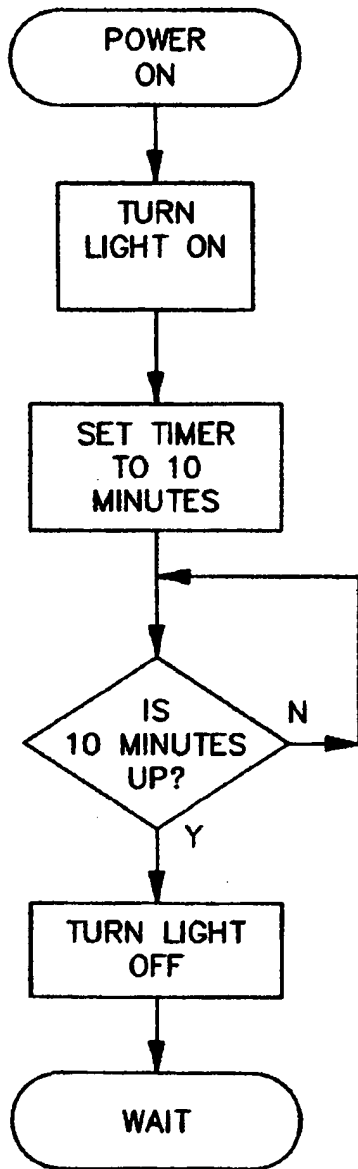
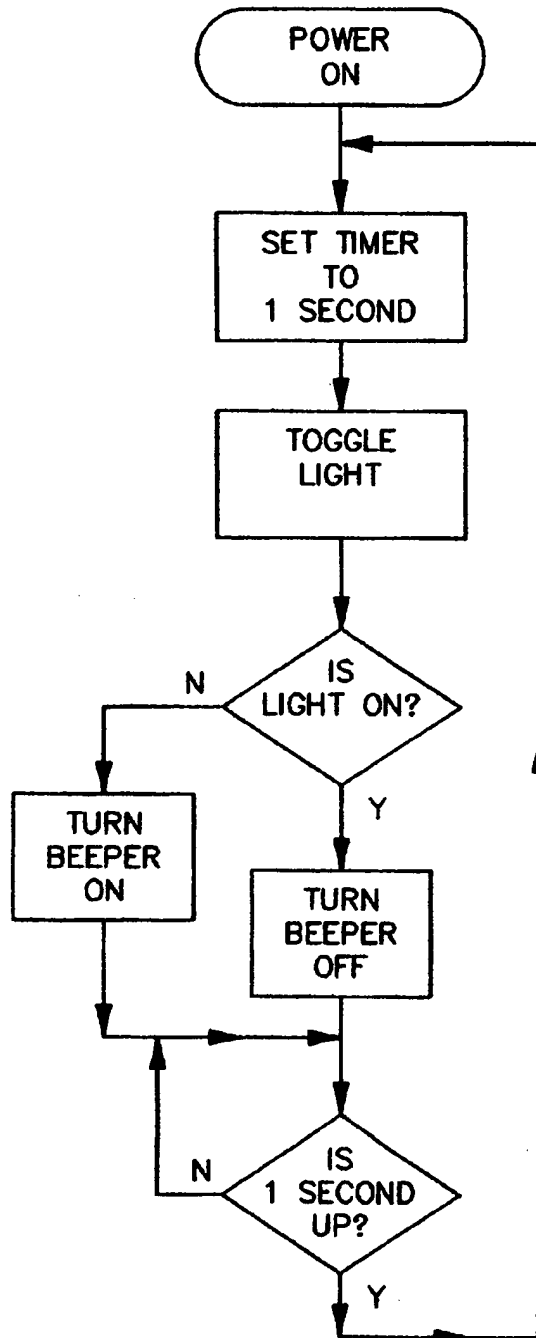


FIG. 18



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FIG. 19

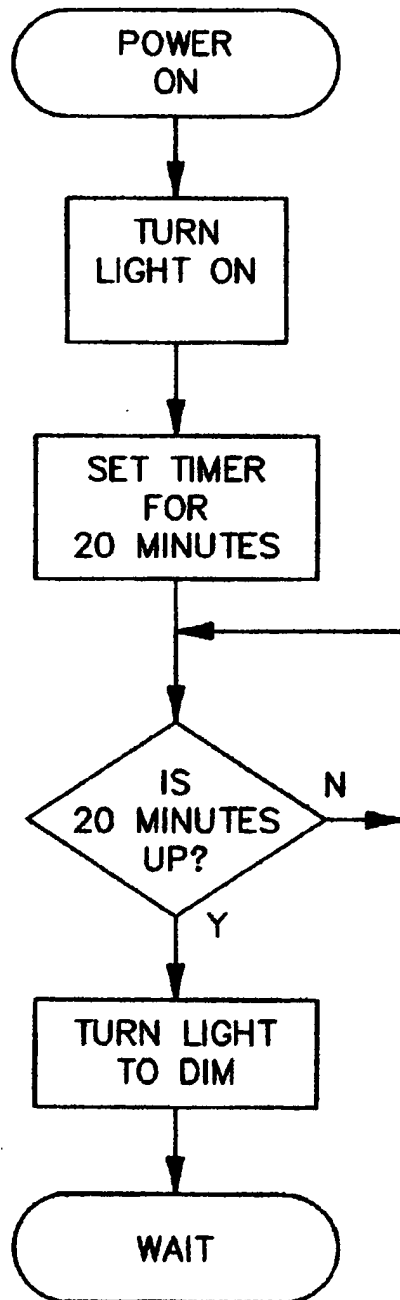
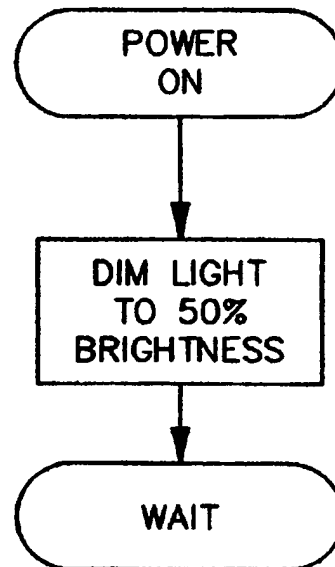


FIG. 20





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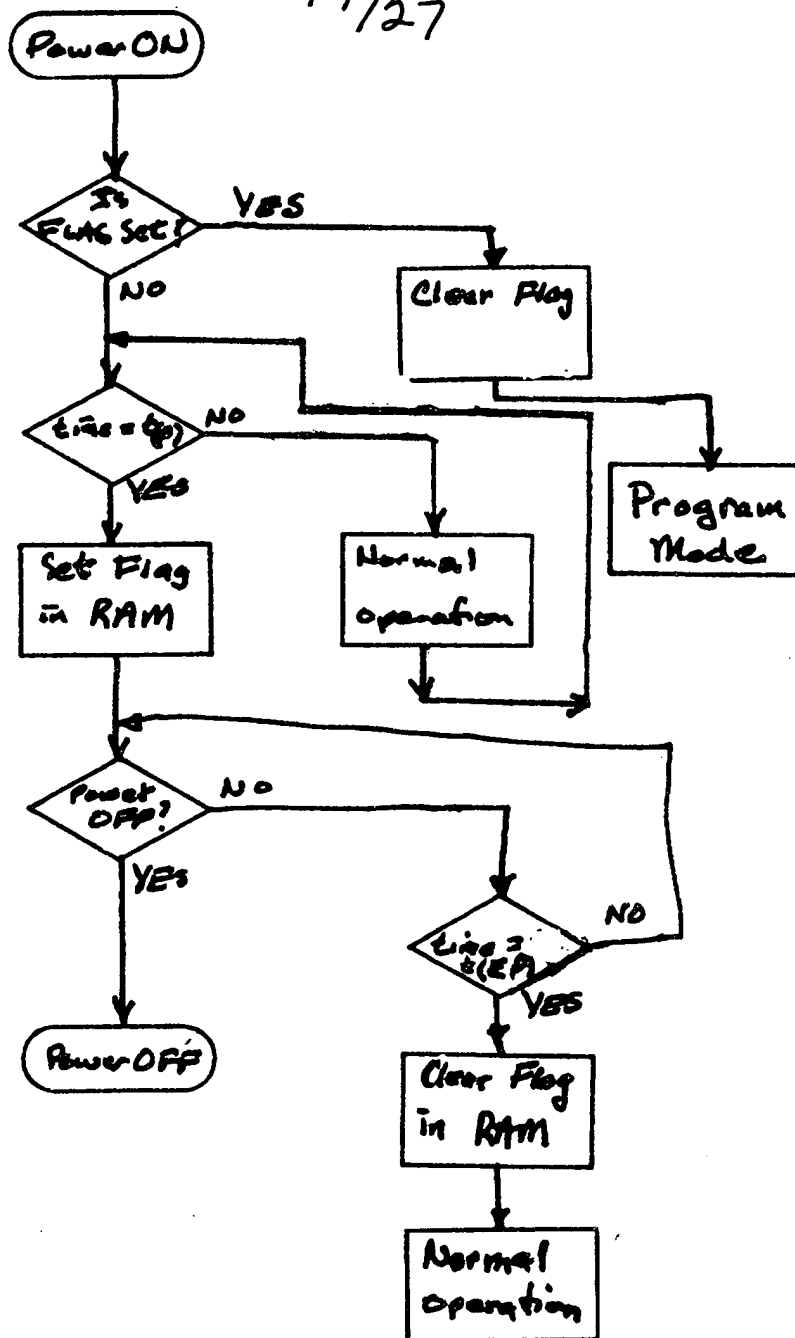


FIG. 21

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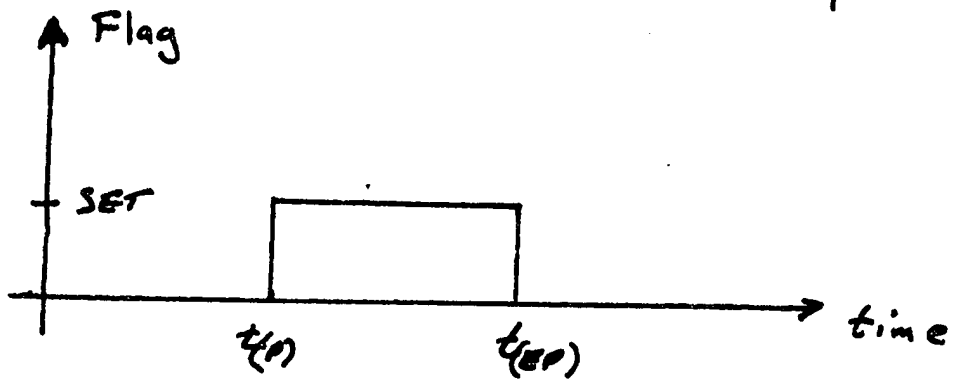


FIG. 22A

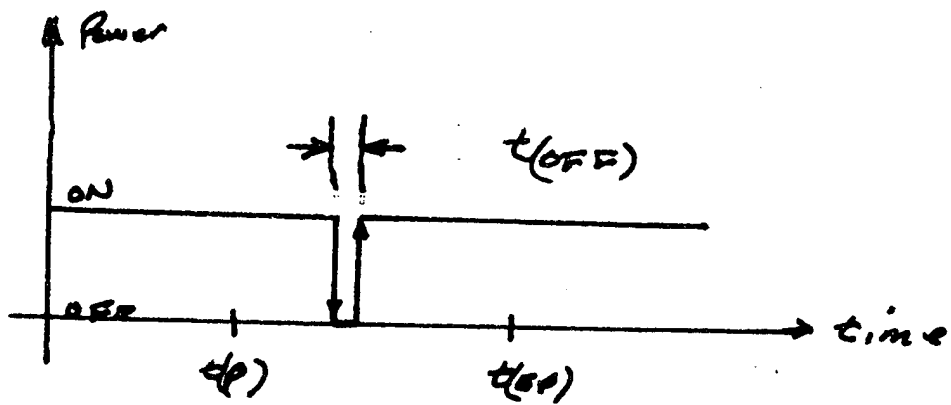


FIG. 22B

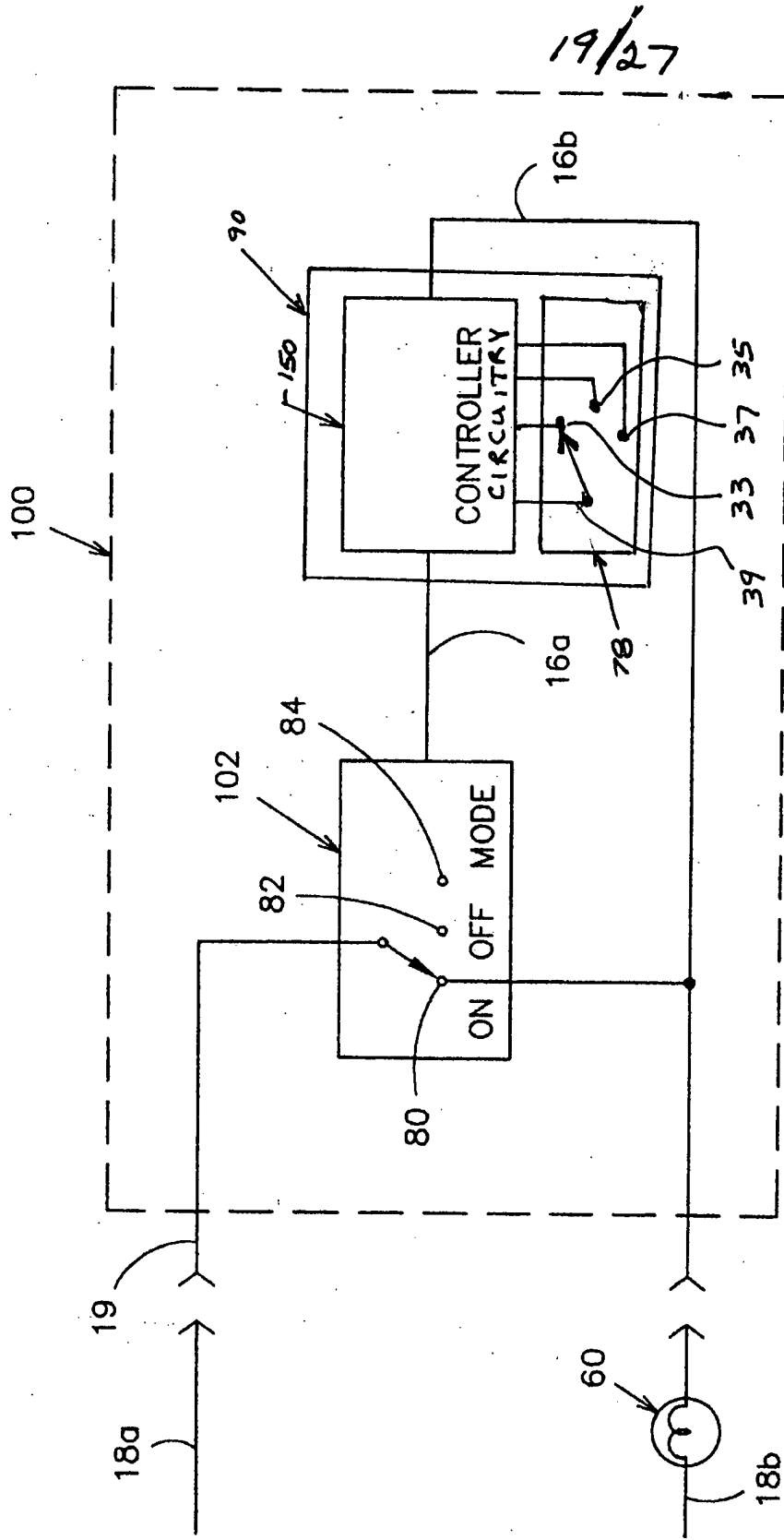


FIG. 23

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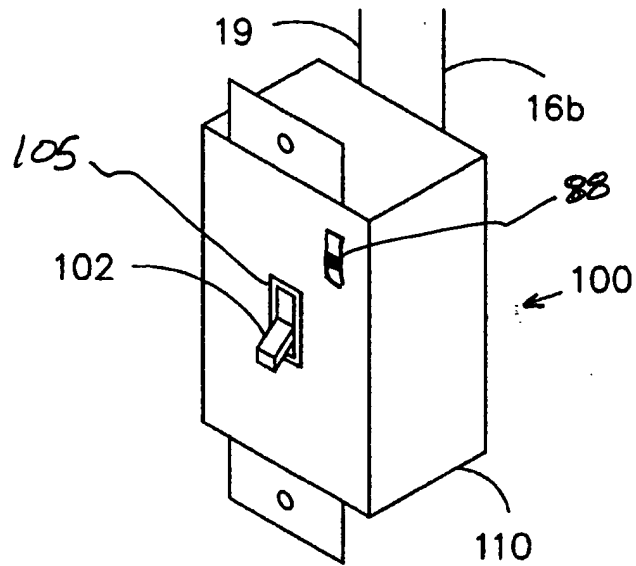


FIG. 24

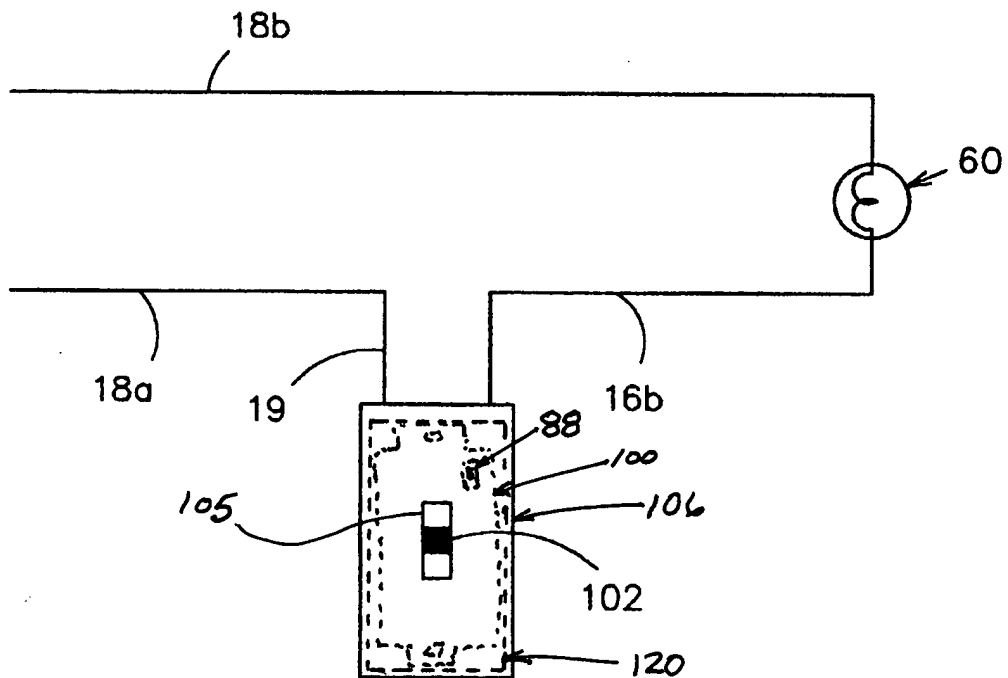


FIG. 25

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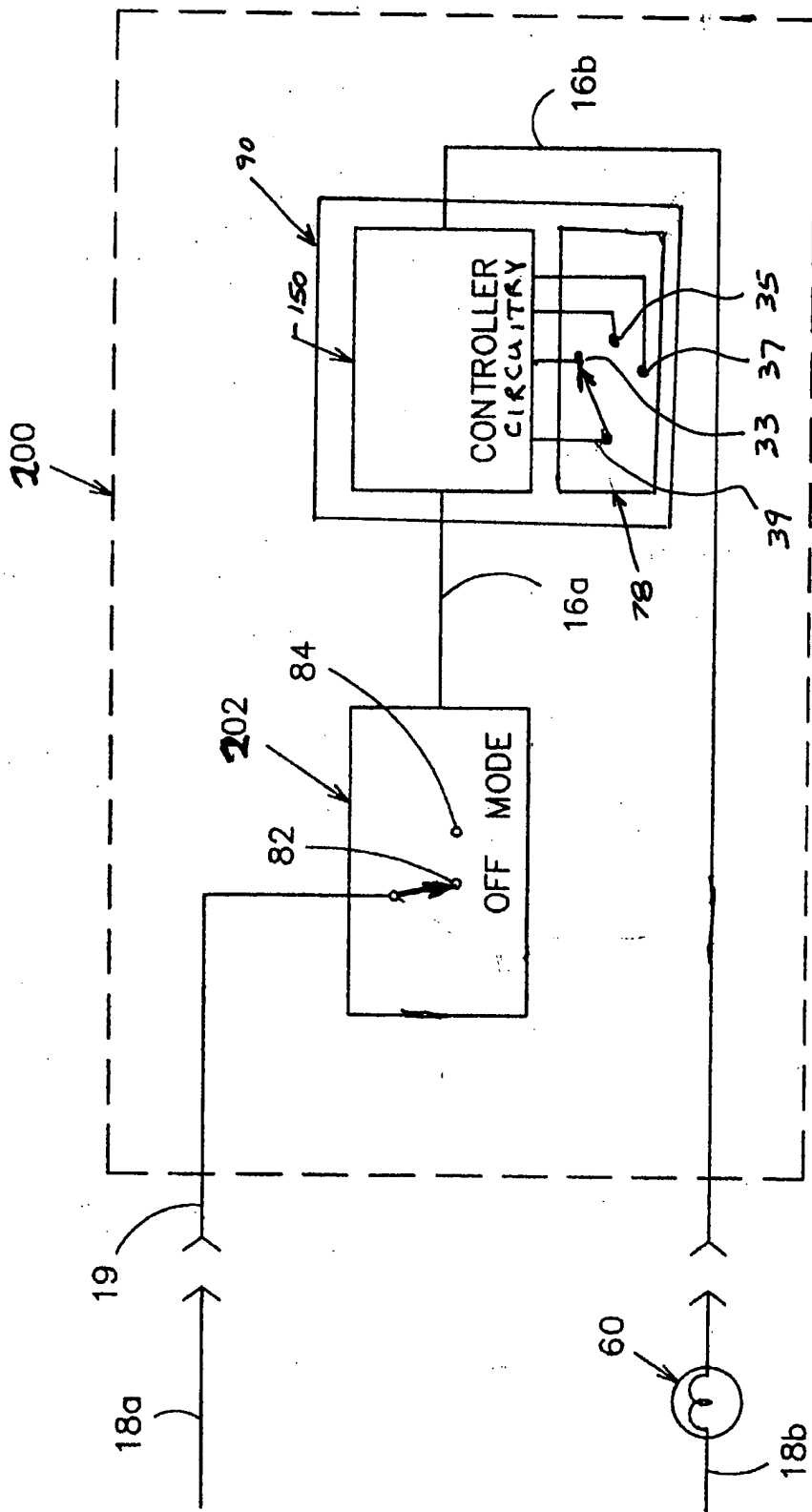


FIG. 26

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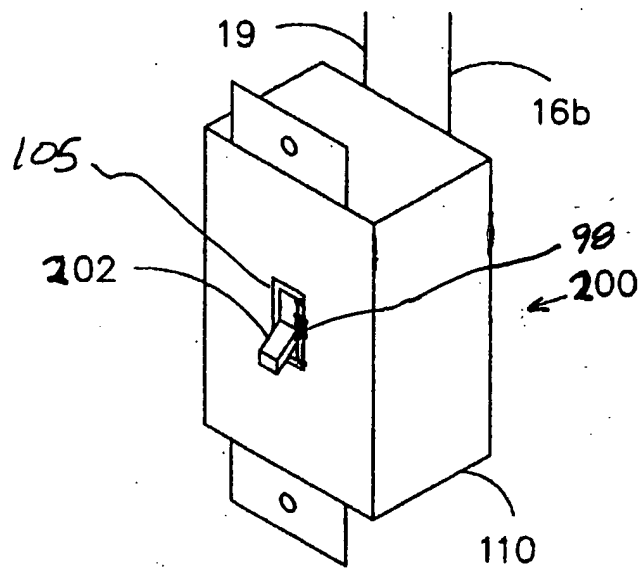


FIG. 27

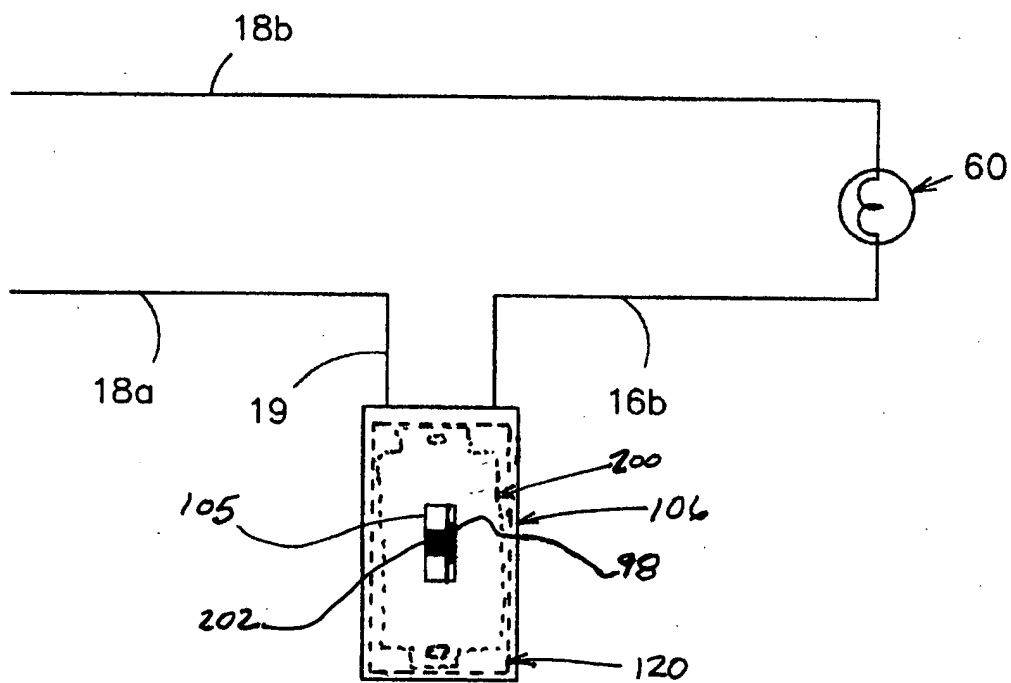


FIG. 28

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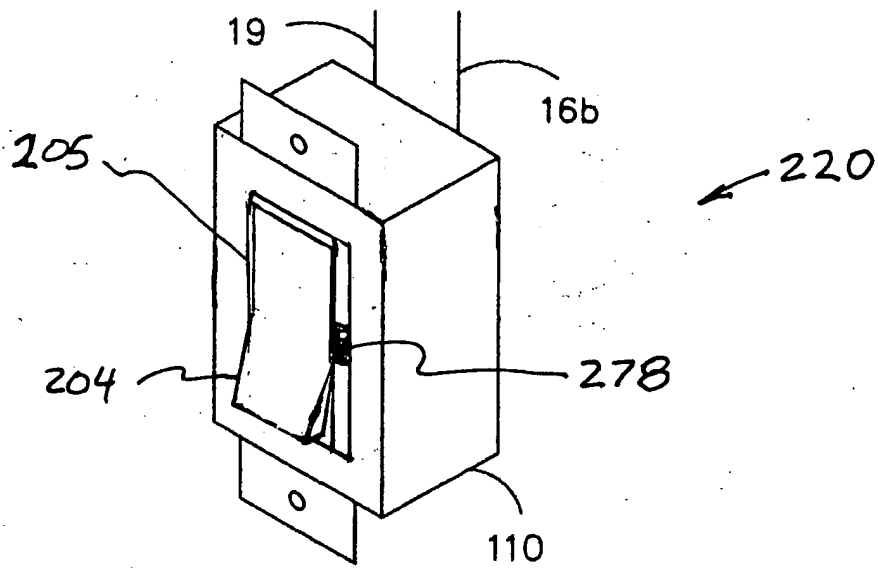


FIG. 29

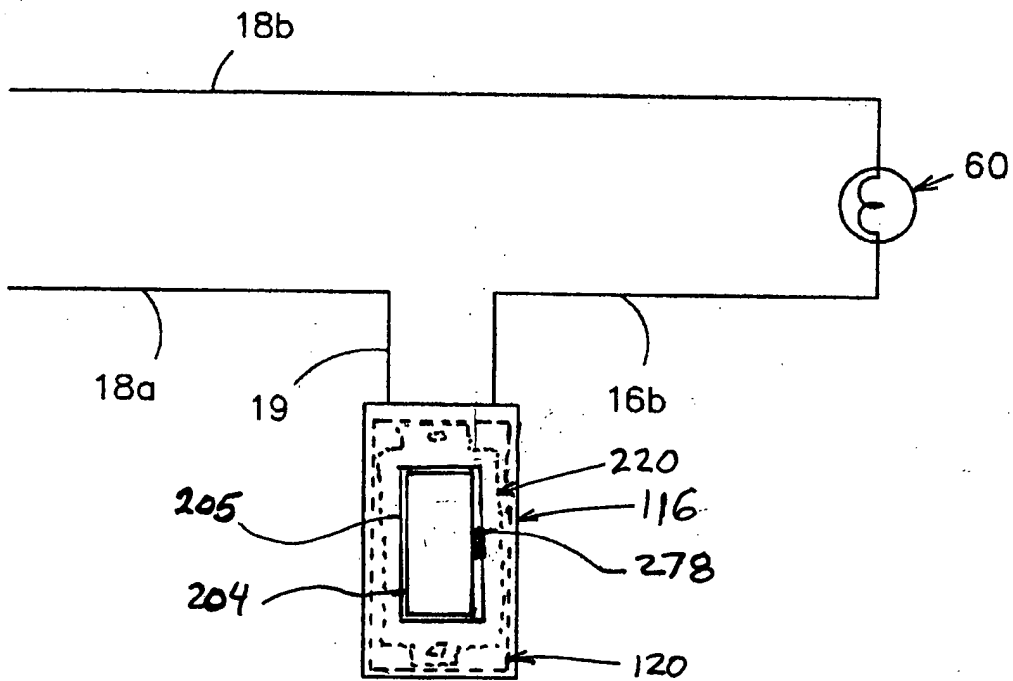


FIG. 30

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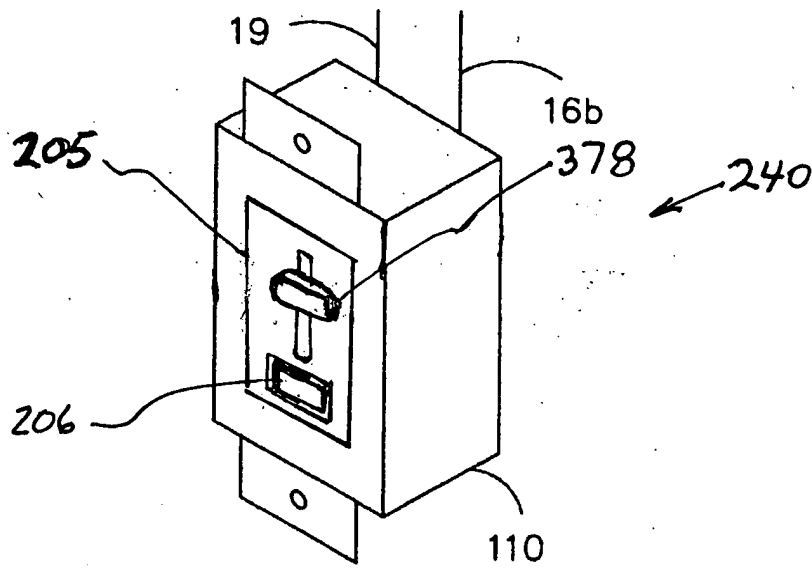


FIG. 31

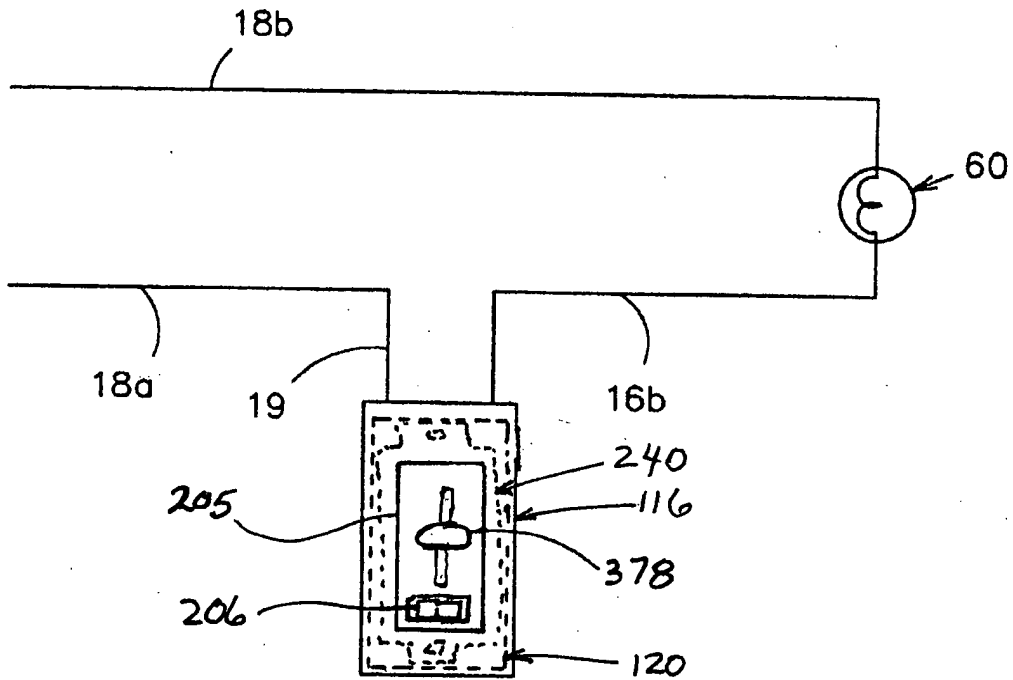


FIG. 32



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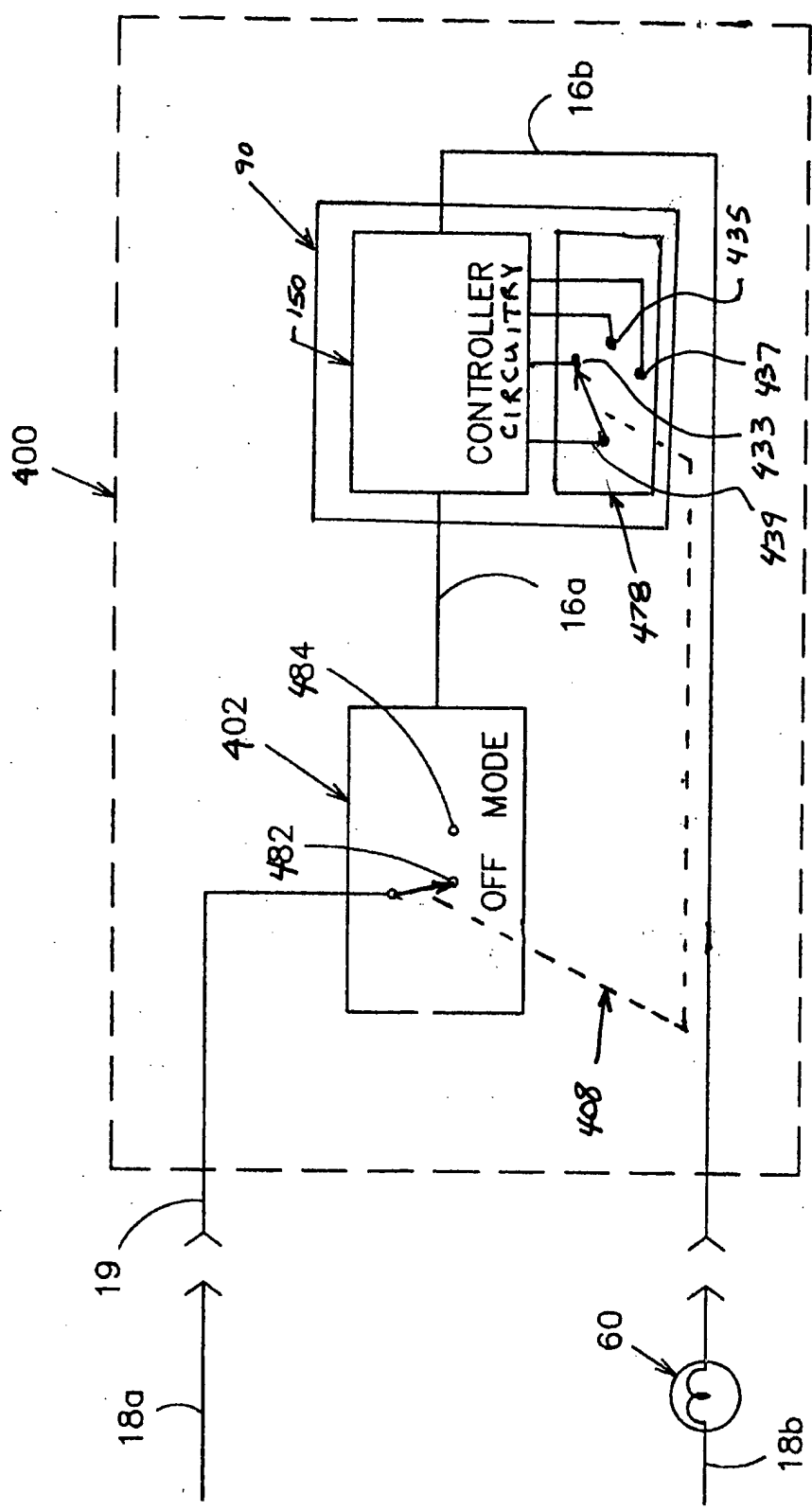


FIG. 33

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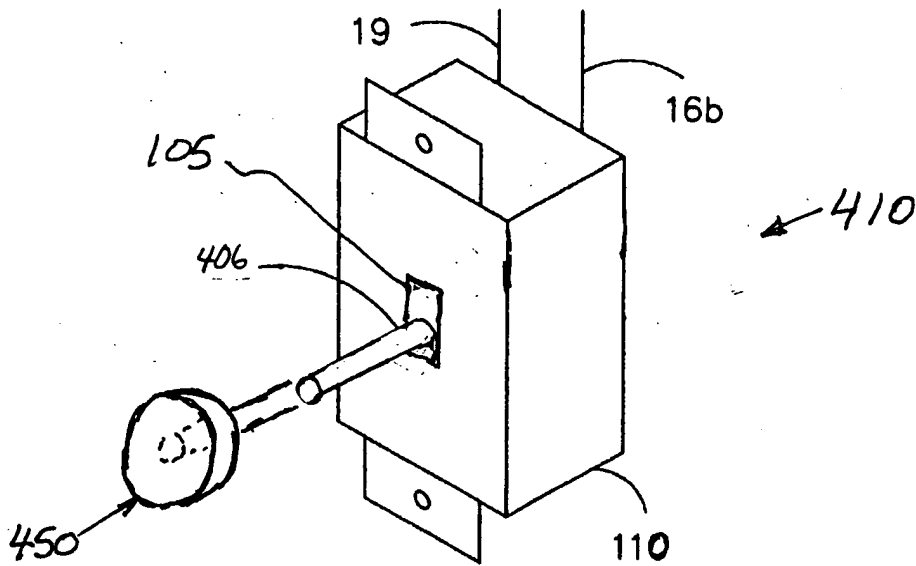


FIG. 34

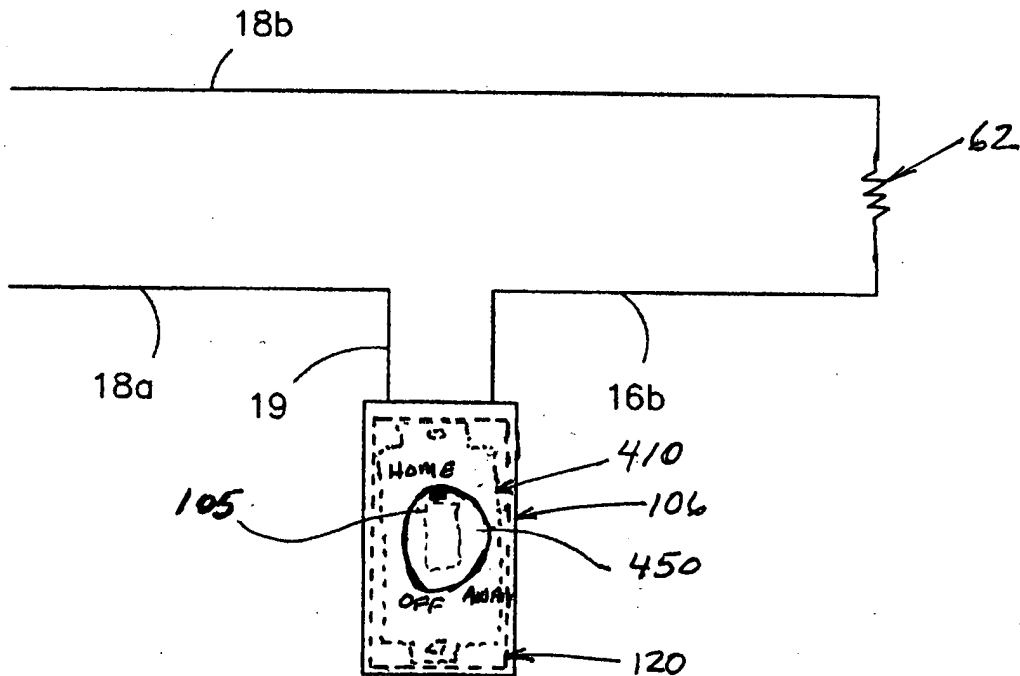


FIG. 35

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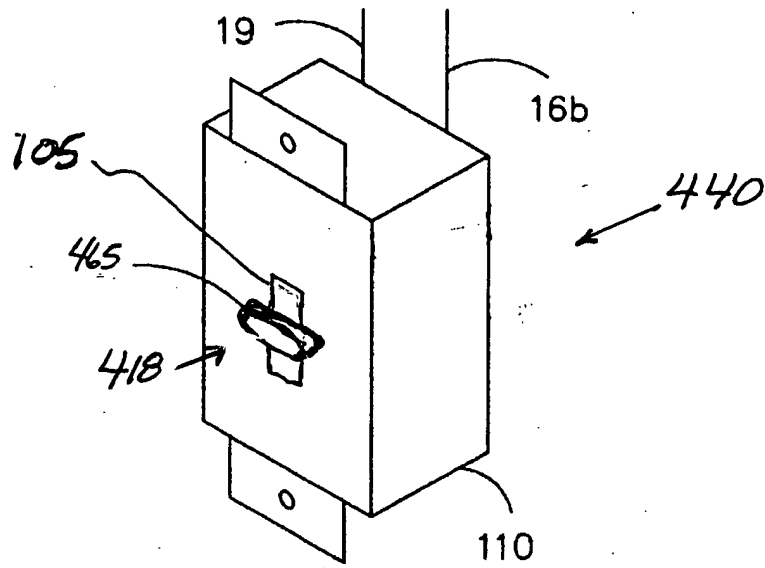


FIG. 36

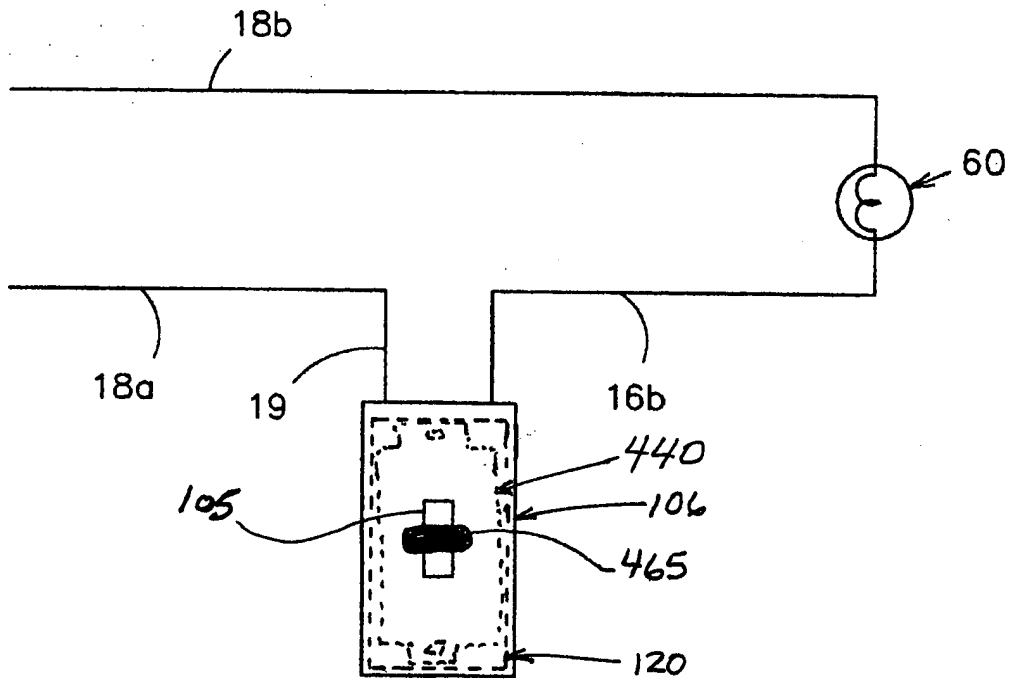


FIG. 37